

# ISL71091SEHXXEV1Z User's Guide

## Introduction

The ISL71091SEHXXEV1Z evaluation boards are designed to measure the performance of the radiation hardened ultra low noise, high precision [ISL71091SEH](#) voltage reference family. There are four output voltage options in the reference family, and they have a wide input voltage range up to 30V and an initial accuracy of 0.05%. With ultra low voltage noise of  $3.3\mu\text{V}_{\text{p-p}}$  in the 0.1Hz to 10Hz range (2.048V option), a maximum output voltage temperature coefficient of  $6\text{ppm}/^\circ\text{C}$ , and excellent radiation performance, the ISL71091SEH is ideal for space applications.

## Specifications

The boards are designed to operate at the following conditions:

- Input voltage range:
  - 2.048V option = 4.2V to 30V
  - 3.3V option = 4.6V to 30V
  - 4.096V option = 6V to 30V
  - 10V option = 12V to 30V
- Output current capability: Source 10mA, Sink 5mA
- Operating temperature range:  $-55^\circ\text{C}$  to  $+125^\circ\text{C}$

## Features

- Small, compact design
- Multiple connectors to easily monitor  $V_{\text{IN}}$  and  $V_{\text{OUT}}$
- Four output voltage options

## Reference Documents

- [ISL71091SEH20](#) Datasheet
- [ISL71091SEH33](#) Datasheet
- [ISL71091SEH40](#) Datasheet
- [ISL71091SEH10](#) Datasheet

## Ordering Information

ORDERING NUMBER	OUTPUT VOLTAGE (V)	TYPE
ISL71091SEH20EV1Z	2.048	Evaluation Board
ISL71091SEH33EV1Z	3.3	Evaluation Board
ISL71091SEH40EV1Z	4.096	Evaluation Board
ISL71091SEH10EV1Z	10.0	Evaluation Board

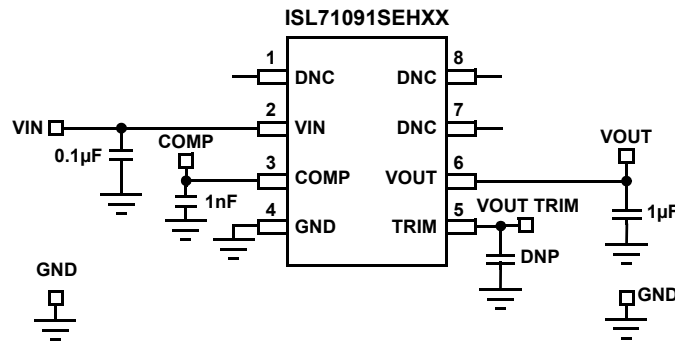


FIGURE 1. ISL71091SEHXXEV1Z BLOCK DIAGRAM

## Functional Description

The schematic of the evaluation board is shown on [page 4](#). The ISL71091SEHXXEV1Z contains the ISL71091SEH voltage reference (U1), input decoupling capacitor (C<sub>1</sub>), compensation capacitor (C<sub>2</sub>), and load capacitor (C<sub>4</sub>). Component values are listed in [Table 1](#).

The power supply leads attach to TP1 and TP2 (VIN, GND). The output is measured at test points TP8 and TP9 (VOUT, GND), and is best measured with a high quality voltmeter in DC conditions. In addition, there is a BNC connector on VOUT to aid in taking measurements such as voltage noise. The SP1 (VIN), SP2 (GND), and SP3 (VOUT) are oscilloscope probe test points for easy connection to an oscilloscope. When testing the output voltage of the device under load, it is recommended to attach the load to the test point turret or BNC connector and monitor the voltage

from the oscilloscope test point. If smaller transient overshoot is desired, increase C<sub>4</sub> to 10μF and C<sub>2</sub> to 10nF. However, note that the settling time also increases.

### TRIM 1-3

The TRIM1, TRIM2, and TRIM3 pins are for factory use only. Do not connect these to the circuit in any way. It will adversely effect the performance of the reference.

## Device Performance

The following plots show the performance of the voltage reference that can be expected on the evaluation board. V<sub>IN</sub> = 8V unless otherwise specified.

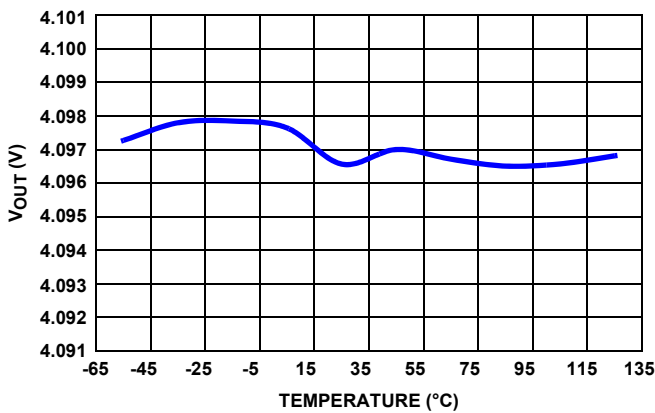


FIGURE 2. V<sub>OUT</sub> vs TEMPERATURE

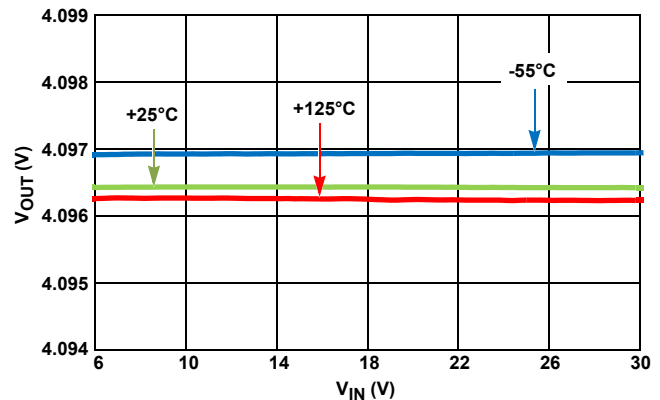


FIGURE 3. LINE REGULATION vs V<sub>OUT</sub> OVER TEMPERATURE

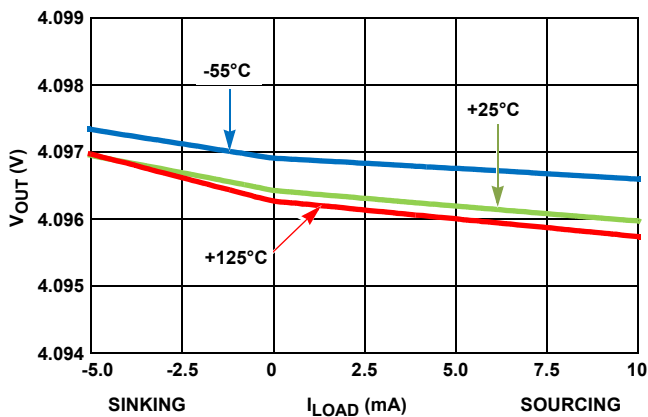


FIGURE 4. LOAD REGULATION vs V<sub>OUT</sub> OVER TEMPERATURE

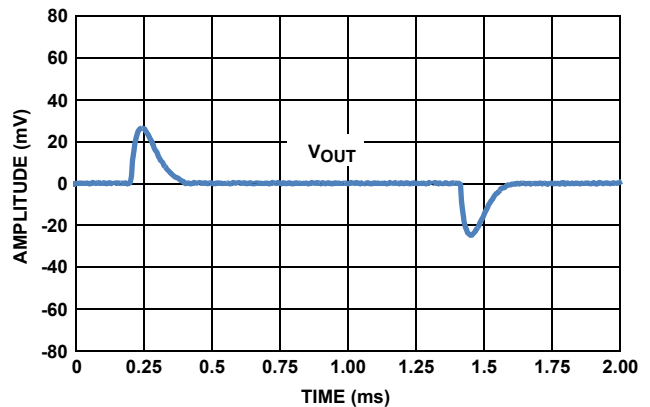


FIGURE 5. LOAD TRANSIENT ( $\Delta I_L = 1\text{mA}$ )

# Application Note 1906

TABLE 1. COMPONENTS PARTS LIST

REFERENCE	VALUE	DESCRIPTION
BNC1		Coaxial Cable PCB Mount Receptacle
C1	0.1 $\mu$ F	Bypass Capacitor 0805, X7R, 50V, 10%
C2	1nF	Compensation Capacitor 0805, X7R, 50V, 10%
C3	DNP	
C4	1 $\mu$ F	Load Capacitor 0805, X7R, 50V, 10%
SP1 - SP3		Scope Probe Test Point PCB Mount
TP1, TP2, TP4, TP5, TP8, TP9		1514-2 Test Point Turret
TP3, TP7, TP8	DNP	
U1	ISL71091SEHXX	Radiation Hardened Ultra Low Noise, Precision Voltage Reference

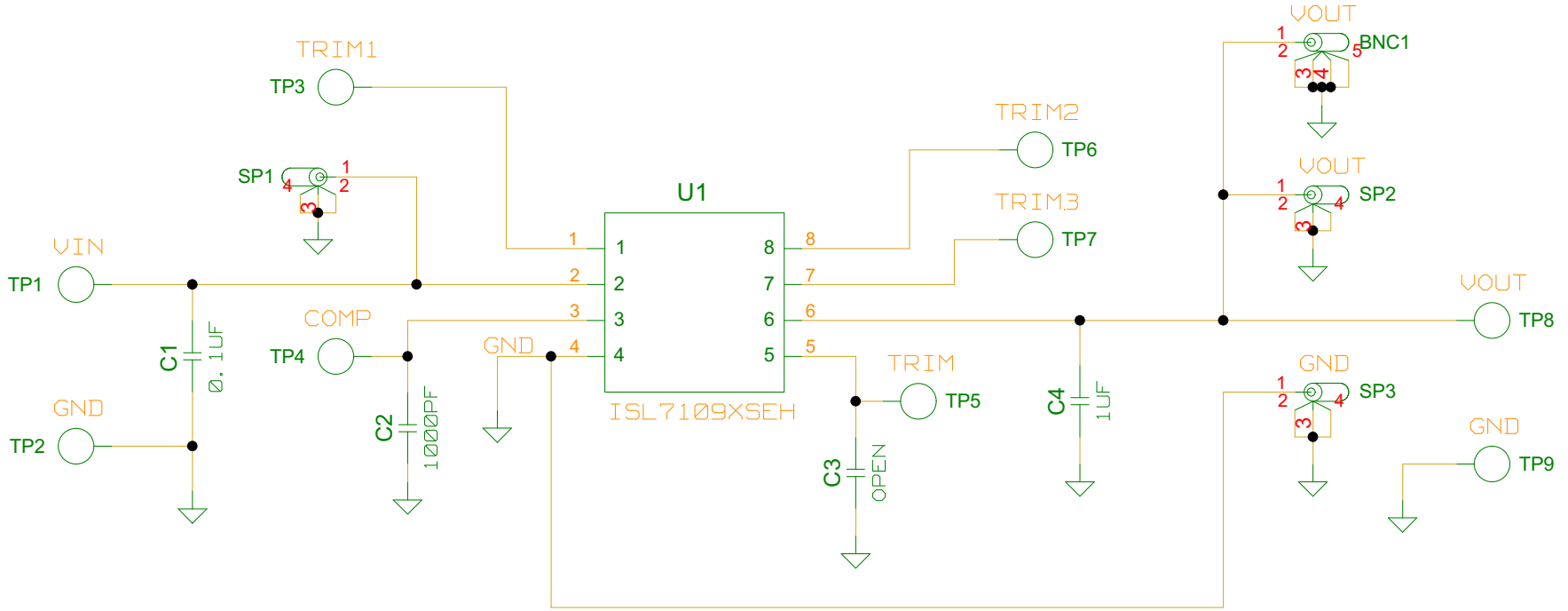


FIGURE 6. VOLTAGE REFERENCE EVALUATION BOARD (FRONT)



FIGURE 7. VOLTAGE REFERENCE EVALUATION BOARD (BACK)

# ISL71091SEHXXEV1Z Schematic



## Voltage Reference Evaluation Board Layout

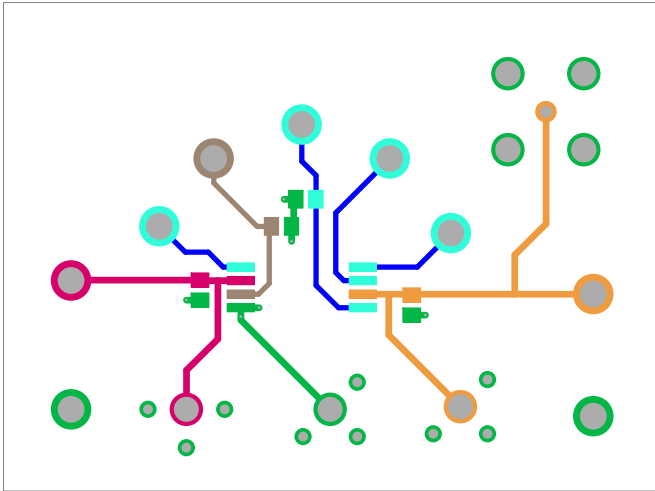


FIGURE 8. TOP LAYER

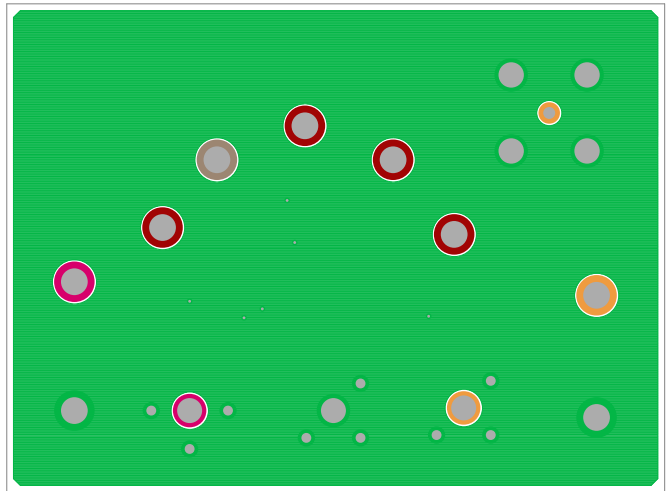


FIGURE 9. BOTTOM LAYER (VIEWED FROM TOP)

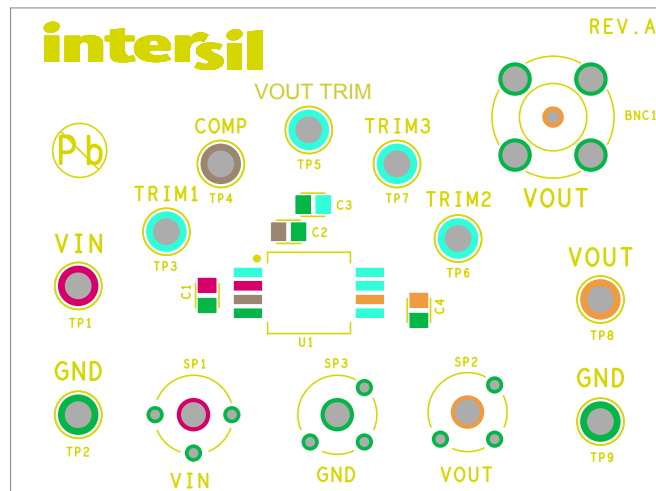


FIGURE 10. TOP SILKSCREEN

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